

AMENDMENTS TO THE SPECIFICATION:

Please amend the specification as follows:

A dead time, in which the GTO elements UP, VP, WP, UN, VN, WN of both of the group shown at the upper positions and that shown at the lower positions are simultaneously brought into a turned-off state, is provided to turn on the GTO elements UN, VN, WN shown at the lower positions after a predetermined interval elapses after the GTO elements UP, VP, WP shown at the upper positions turn off. Consequently, both of the group of the GTO elements UP, VP, WP shown at the upper positions and that of the GTO elements UN, VN, WN shown at the lower positions are prevented from simultaneously being put into a turned-on state. Thus, an occurrence of a dc shortcircuit is prevented (see, for example, Page 3, line 14, please remove ~~Patent Document 1.~~ and add JP-A-10-112984).